

# Monolithic Phase-Locked Loops And Clock Recovery Circuits: Theory And Design

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Monolithic Phase-locked Loops and Clock Recovery Circuits Theory and Design.

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Monolithic Phase Locked Loops and Clock Recovery Circuits by Behzad Razavi(ed.) Monolithic Phase Locked Loops and Clock Recovery Circuits plot design, simulation

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**Voltage-controlled oscillator - Wikipedia, the -**

6 Voltage-controlled crystal oscillator as a clock generator; 7 See Monolithic function generators are voltage-controlled. Analog phase-locked loops typically

[http://en.wikipedia.org/wiki/Voltage-controlled\\_oscillator](http://en.wikipedia.org/wiki/Voltage-controlled_oscillator)

**IET Digital Library: Design of loop filter in -**

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**Relaxation oscillator - Wikipedia, the free -**

The circuit consists of a feedback loop The first relaxation oscillator circuit, and electronic beepers. and clock signals in some digital circuits.

[http://en.wikipedia.org/wiki/Relaxation\\_oscillator](http://en.wikipedia.org/wiki/Relaxation_oscillator)

**Design of Monolithic PhaseLocked Loops and Clock -**

This paper describes the principles of phase-locked system design with emphasis on monolithic implementations. Following a brief review of basic concepts, we analyze

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**Design of high-performance CMOS charge pumps in -**

Monolithic Phase Locked Loops and Clock Recovery Circuits (Citations: 89) Unknown Published in 1996. Charge-Pump Phase-Lock Loops (Citations: 374) FLOYD M. GARDNER.

<http://academic.research.microsoft.com/Paper/2393244.aspx>

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**Design of high-speed clock and data recovery -**

architectures for a high-speed clock and data recovery Monolithic phase-locked loops and clock loops and clock recovery circuits, theory and design.

<http://link.springer.com/article/10.1007/s10470-007-9093-1>

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4.1 Clock recovery; When Signetics introduced a line of monolithic integrated circuits like the NE565 that were Phase-Locked Loop Circuit Design

[http://en.wikipedia.org/wiki/Phase-locked\\_loop](http://en.wikipedia.org/wiki/Phase-locked_loop)

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### **Patent US4987373 - Monolithic phase- locked loop - -**

A phase-locked loop Patents Publication FIGS. 7 and 8 illustrate the preferred embodiment as implemented in a CMOS monolithic circuit. CLOCK GENERATOR.

<http://www.google.com/patents/US4987373>

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